

THYRISTOR HAVING ONE OR MORE  
DOPED LAYERS

By: Tatsing Paul Chow and Jeffrey Bernard Fedison

Attorney Docket No.: R0434/7008 EJR

August 31, 2001  
EL 711222020 US\$

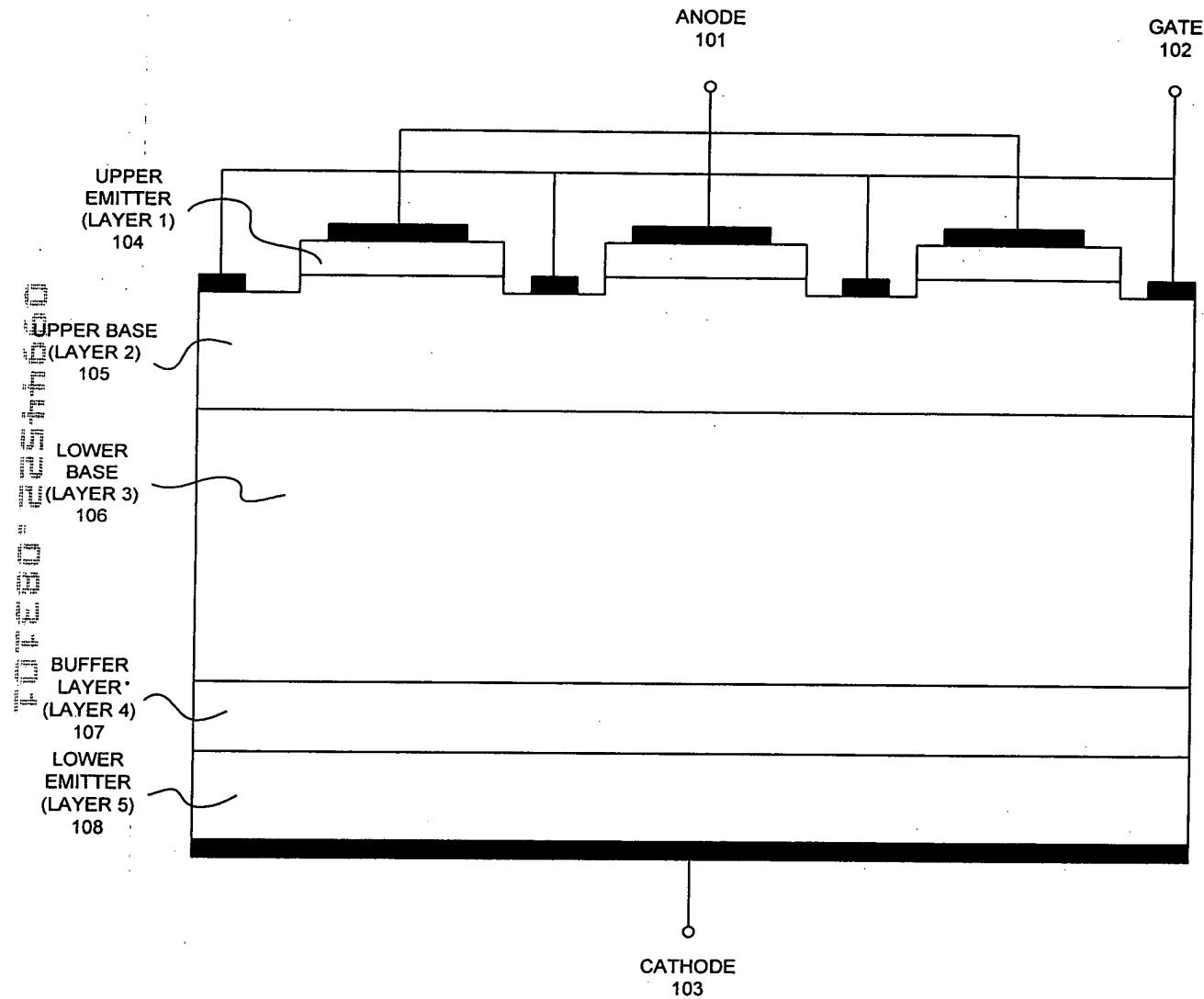


FIGURE 1

TRANSISTOR HAVING ONE OR MORE  
DOPOED LAYERS  
By: Tatsing Paul Chow and Jeffrey Bernard Fedison  
Attorney Docket No.: R0434/7008 EJR  
August 31, 2001  
EL 71122Z020up

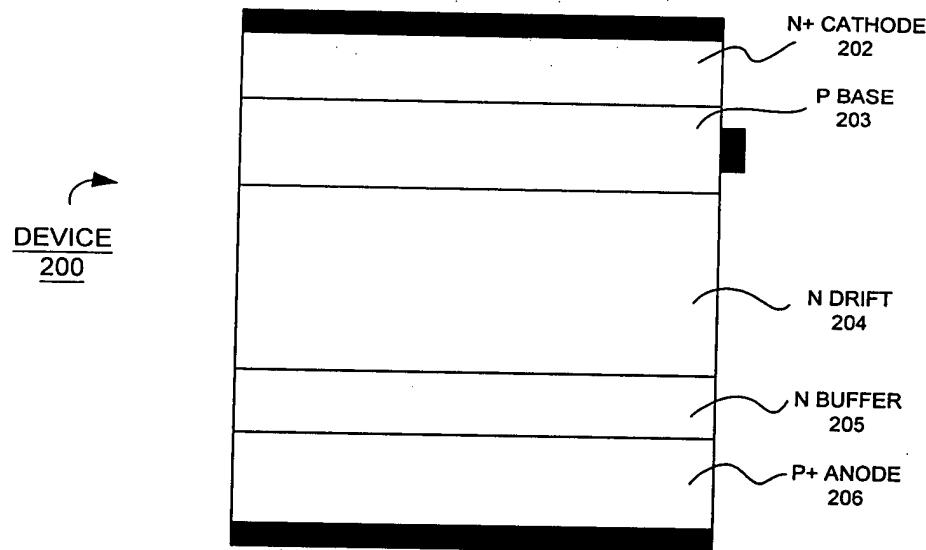


FIGURE 2A

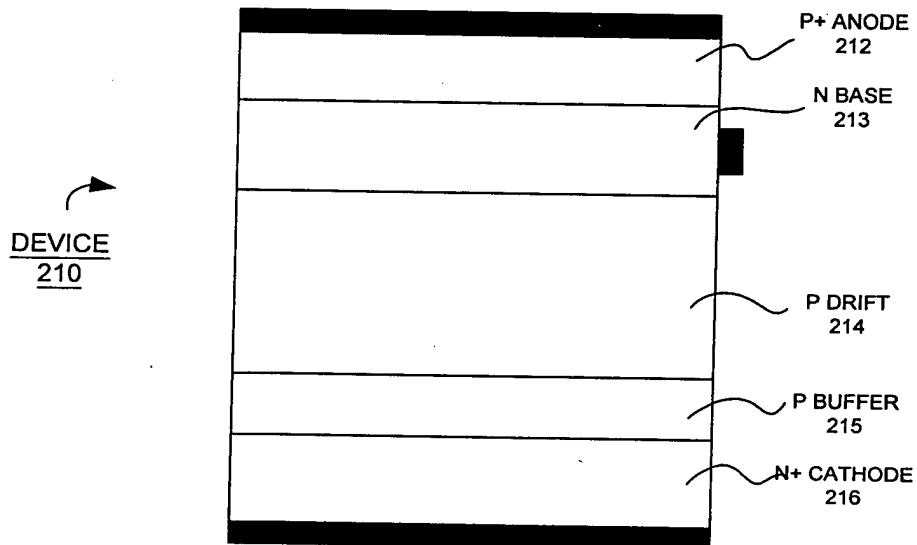


FIGURE 2B

THYRISTOR HAVING ONE OR MORE  
DOPED LAYERS

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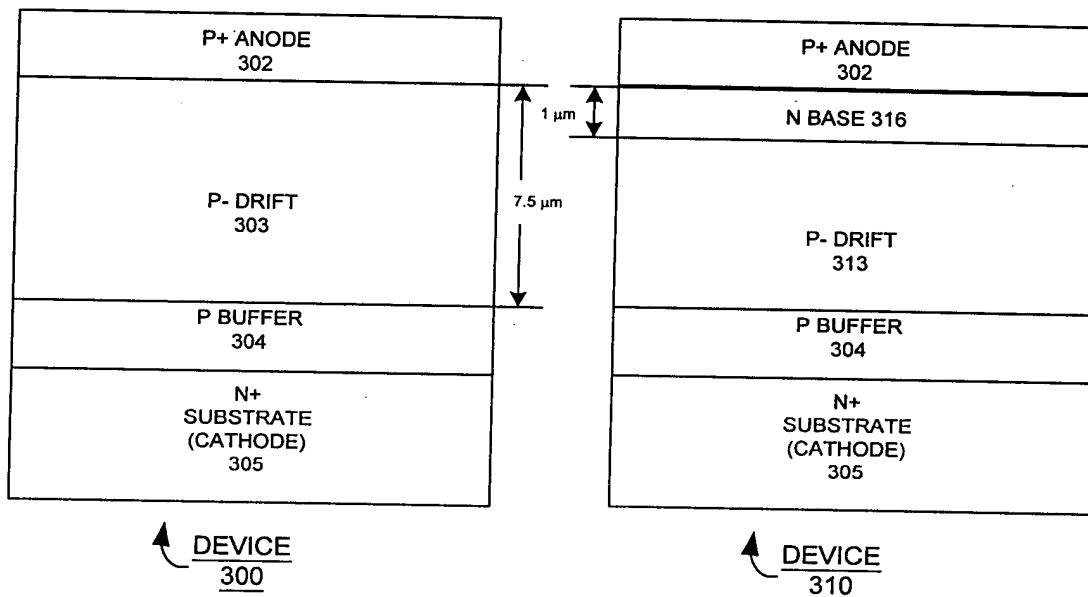


FIGURE 3A

FIGURE 3B

HYRISTOR HAVING ONE OR MORE  
DOPED LAYERS

By: Tatsing Paul Chow and Jeffrey Bernard Fedison  
Attorney Docket No.: R0434/7008 EJR  
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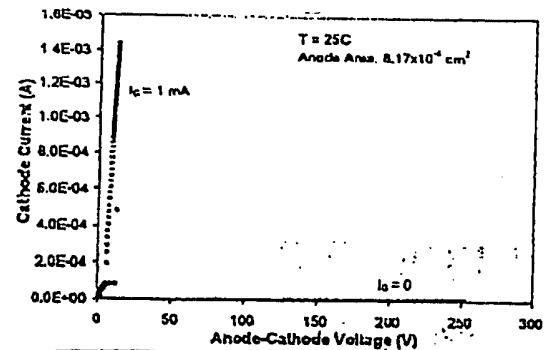


FIGURE 4

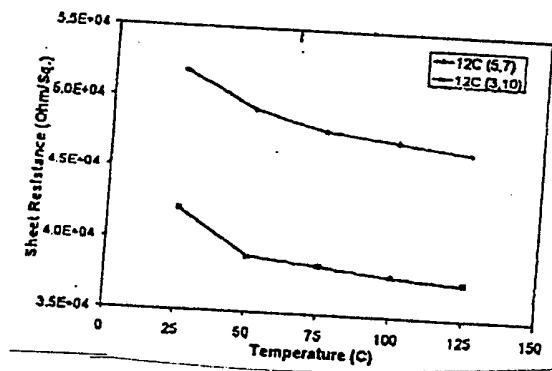


FIGURE 5